

# Notice

## **B.Tech Degree VII Semester Laboratory (Supplementary) Examination May 2017**

### **2006 Scheme EC 707 – Communication Laboratory – II**

Date	Time	Register No	Total
7.6.17 Wednesday	10:00 AM – 1:00 PM	13120876	1

### **2012 Scheme EC17L2 – Communication Laboratory – II**

Date	Time	Register No	Total
9.6.17 Friday	10:00 AM – 1:00 PM	13130863 13130909 13130921 13130931 13140820 13140846 13140929 13104933	8

### **2012 Scheme EC17L1 – Embedded Systems Laboratory**

Date	Time	Register No	Total
12.6.17 Monday	10:00 AM – 1:00 PM	13130863 13130869 13130909 13130921	4

Above students are directed to report to the Communication and Microprocessor Labs for the exam on the allotted dates at 9.45 AM.

**Dr Vinu Thomas  
Professor and Head**