

Model Engineering College, Thrikkakara
Department of Computer Engineering

B.Tech Degree VII Semester Computer Science And Engineering, June 2017
CS 17L1 LANGUAGE PROCESSORS LABORATORY
SPECIAL SUPPLEMENTARY EXAMINATION SCHEDULE

| Date | Venue | Time | Reg Number |
|-------------------------|--------------|-------------|-------------------|
| 09/06/2017 (FRIDAY) | CC2 | FN | 12130807,12120864 |

HOD